

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al.                      Art Unit : Unknown  
Serial No. : New Divisional Application                      Examiner : Unknown  
Filed : October 27, 2003  
Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREOF

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## **APPENDIX**

- Annotated Sheet of Drawings showing changes marked in red (1 page)
- Replacement Sheet (1 page)

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Fig.3A

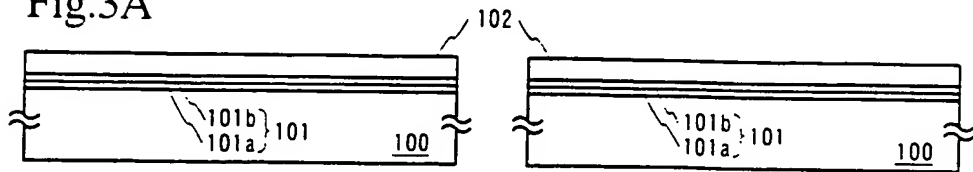


Fig.3B

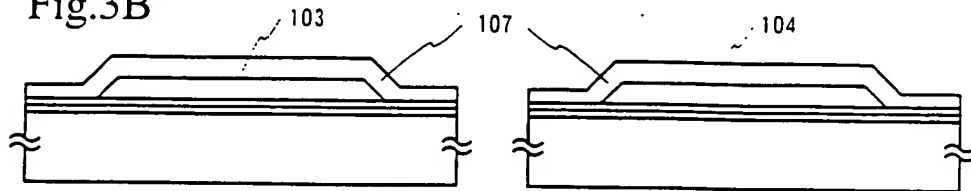


Fig.3C

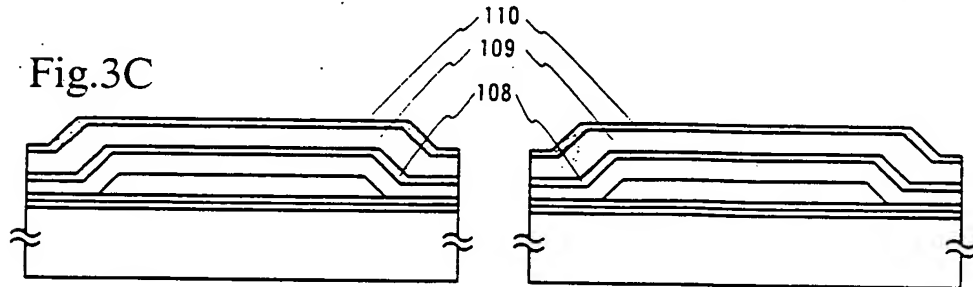


Fig.3D

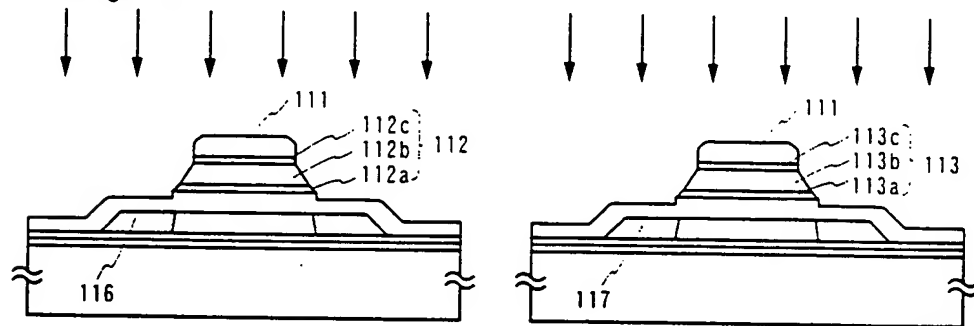


Fig.3E

